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12390 EL CAMINO REAL  
San Diego, California  
92130

Telephone  
858 678-5070

Facsimile  
858 678-5099

Web Site  
www.fr.com

Date December 16, 2003

To Examiner Hong Kim

Telephone: (703) 305-3835

Facsimile number 10559-36400001 / (703) 746-7240

From Linda G. Gunderson

Re P8247X METHOD AND APPARATUS USING DUAL-PORTED MEMORY FOR  
PERFORMANCE ENHANCEMENT

Your Ref.: 09/672,345

Our Ref.: 10559-364001

Number of pages  
including this page 2

Message Dear Examiner Kim,  
Here is a proposed agenda for an interview on this case. I need to respond/RCE before  
Christmas, so if we could discuss the case early next week I'd appreciate it.  
Thanks, and feel free to call me at (858) 678-4311 if you have any questions.  
Linda

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information. If you have received this facsimile in error, please immediately call us collect at  
858 678-5070 to arrange for its return. Thank you.

# DRAFT

## Proposed interview agenda

Application/Control Number 09/672,345

1. Discuss the support in the specification for the feature:  
"wherein the computer system is configured so that control accesses from the central processing unit are directed to the multi-ported memory and not to the main memory and data accesses from the central processing unit are directed to the main memory and not to the dual ported memory" of claim 1.
2. Discuss the differences between the claims and Dinwiddie (U.S. Patent No. 4,371,932), particularly discuss which elements of Dinwiddie are being identified with the main memory and which elements are being identified with the multi-ported memory.
3. Discuss a claim structure to clearly differentiate the multi-ported memory from the registers and handshake, interrupt, and miscellaneous controls elements of Dinwiddie.

## Proposed new claim for discussion:

34. (new) A data processing system, comprising:
  - a processor;
  - a main memory;
  - a multi-ported memory in communication with the processor and the main memory, the multi-ported memory having a storage capacity of about 4 kilobytes or greater; and

wherein the system is configured to receive a request to write information to a memory location, wherein the information has an information type equal to data or control information, and wherein the system is further configured to determine a memory destination among the main memory or the multi-ported memory based on the information type.

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